

SRA2202K

PNP Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

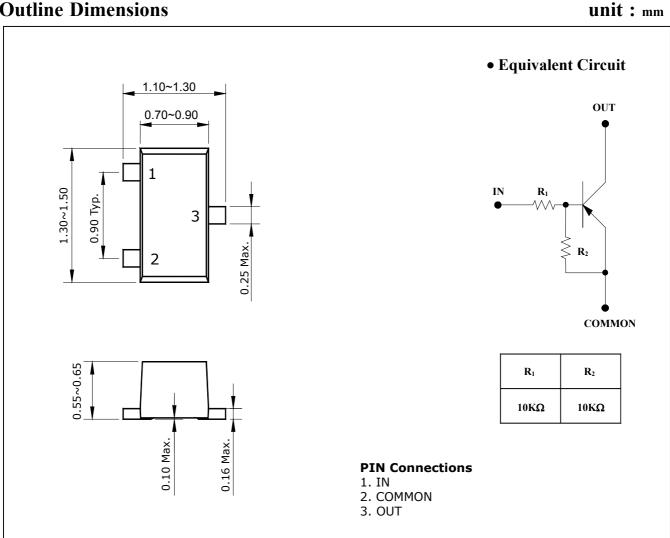
Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

Ordering Information

Type NO.	Marking	Package Code		
SRA2202K	2R	SOT-623F		

Outline Dimensions



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Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V _I	-30, 10	V
Output current	I_{O}	-100	mA
Power dissipation	P_{D}	100	mW
Junction temperature	T _J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

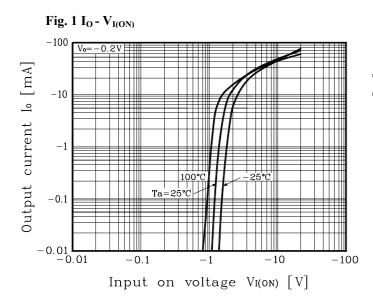
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_I = 0$	ı	ı	-500	nA
DC current gain	G_{I}	V _O =-5V, I _O =-10mA	50	80	-	ı
Output voltage	$V_{O(ON)}$	I_O =-10mA, I_I =-0.5mA	ı	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$, $I_0 = -5mA$	ı	-1.8	-2.4	٧
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	f_T^*	V_O =-10V, I_O =-5mA, f=1MHz	ı	200	ı	MHz
Input current	I_{I}	V_{I} =-5V, I_{O} =0	1	1	-0.88	mA
Input resistor (Input to base)	R_1	-	7	10	13	K Ω
Input resistor (Base to common)	R ₂	-	7	10	13	K Ω

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves



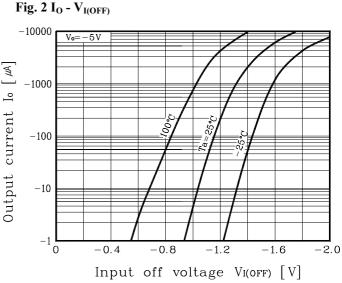
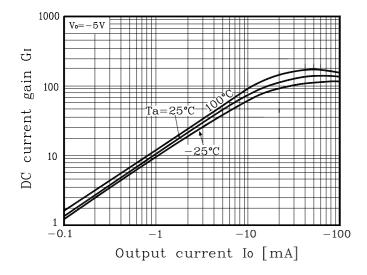


Fig. 3 G_I - I_O



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